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Our File No. 10125/4122
LGP Ref. No. F03-316US001

PATENT APPLICATION TRANSMITTAL LETTER

To the Commissioner for Patents:

Transmitted herewith for filing is the patent application of Joeng-Rok Kim et al.
for TRANSISTOR ARRAY SUBSTRATE FABRICATION FOR AN LCD.

031283 U.S. PTO
10/712939
111303

Enclosed are:

- ☒ 21 pages of application (including title page), 5 (sheet(s) of [formal] drawings (Figs. 1-5) and the following Appendices:
None
- ☐ Assignment transmittal letter and Assignment of the invention to : LG. PHILIPS LCD CO., LTD.
- ☒ Declaration Combined Power of Attorney
- ☒ Submission of Certified Copy of Priority Document and certified copy of Korean Application No. 2002-86112
- ☐ Information Disclosure Statement, PTO-1449, and reference A

Claims as Filed	Col. 1	Col. 2
For	No. Filed	No. Extra
Basic Fee		
Total Claims	20-20	0
Indep. Claims	4-3	1
Multiple Dependent Claims Present		

*If the difference in col. 1 is less than zero,
enter "0" in col. 2.

Small Entity	
Rate	Fee
	\$ 385
x\$9=	\$
x\$42=	\$
+\$140=	\$
Total	\$

Other Than Small Entity	
Rate	Fee
	\$ 770
x\$18=	\$
x\$86=	\$86
+\$290=	\$
Total	\$856

- ☐ Please charge my Deposit Account No. 23-1925 in the amount of \$_____. A duplicate copy of this sheet is enclosed.
- ☒ A check in the amount of \$856 to cover the filing fee is enclosed.
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 - ☐ Any patent application processing fees under 37 CFR § 1.17.
 - ☐ The issue fee set in 37 CFR § 1.18 at or before mailing of the Notice of Allowance, pursuant to 37 CFR § 1.311(b).

Date

11/13/03

Anthony P. Curtis, Ph.D.

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